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board serves, space is always at a premium on a printed circuit board. This would particularly be the case for a baseboard (motherboard) in which a microprocessor, memory, communications interface, and peripheral interfaces are attached thereto. However, it would also be the case for the peripheral and communication's interfaces that would often be placed on separate boards. Further, the printed circuit board serves the primary function of establishing communications between chips placed on the printed circuit board and possibly other boards. Therefore, a paramount concern in printed circuit board design is the communications and power lines and their layout on the surface of the printed circuit board or in the embedded layers of the printed circuit board and communications between one layer and another in the printed circuit board.--

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Please replace the paragraph beginning on page 8, line 1 with the following rewritten paragraph:

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--FIG. 7 is a top view of another example of a printed circuit board using the embodiments of the present invention shown in figures 4A through 5B. FIG. 7 is similar to FIG. 6 with the exception that certain leads/traces 40 connect to a common connector lead contained within switch 30. Therefore, a single positive thermal coefficient switch may be placed in or surface mounted to switch 30 and support several leads/traces 40 without the need for individual leads/traces 40 on the printed circuit board. Thus by being able to support multiple leads/traces 40 with a single positive thermal coefficient switch significant savings of space and money may be realized utilizing the embodiments of the present invention.--